

Listing of Claims

1. (Currently Amended) A circuit arrangement for filtering and/or selecting single frequencies or frequency ranges of signals, said circuit arrangement (100) comprising at least two electric resonant circuits (10; 20; 30), each resonant circuit comprising

- ~~with~~ at least an inductive element (12; 22; 32) and
- at least a capacitive element; having a capacitance other than the parasitic capacitance of remaining portions of the circuit (14; 24; 34) arranged parallel to said at least one inductive element,

characterized in that the resonant circuits (10; 20; 30) are magnetically fixedly coupled to each other, and in that all the resonant circuits (10; 20; 30) of the circuit arrangement (100) are arranged on only one metallization plate (40) of an integrated circuit, having an essentially constant ohmic resistance, said metallization plate being one of plural, and being the one having the least ohmic resistance.

2. (Previously Cancelled)

3. (Previously Provided) A circuit arrangement as claimed in claim 1, characterized in that the individual resonant circuits (10; 20; 30) are essentially arranged in a planar way on an outer surface area of the integrated circuit, and being arranged as concentrically aligned inductors.

4. (Previously Cancelled)

5. (Currently Amended) A circuit arrangement as claimed in claim 4 1, characterized in that the geometric structure is a circle, an oval, an ellipse, a square or a rectangle.

6. (Previously Cancelled)

7. (Currently Amended) A circuit arrangement as claimed in claim 1, characterized in that at least two inductive elements (12; 22; 32) which are substantially concentric and/or substantially parallel to each other, said Said capacitive elements are arranged linearly with respect to each other and number at least three.

8-11. (Previously Cancelled)

12. (Previously Provided) A circuit arrangement as claimed in claim 1, characterized in that the inductive elements (12; 22; 32) are magnetically fixedly coupled to each other.

13. (Previously Provided) A circuit arrangement as claimed in claim 7, characterized in that each of the inductive elements (12; 22; 32) comprises one turn.

14. (Previously Provided) A circuit arrangement as claimed in claim 7, characterized in that each of the inductive elements (12; 22; 32) comprises a plurality of turns.